



PanIIT USA Board Member

Sagar Pushpala



Bio of Sagar Pushpala

Sagar is currently a strategic advisor with a broad technology and operations background. He was previously CEO of TSI Semiconductors, a niche, specialty analog mixed-signal foundry focused on technologies and manufacturing services serving emerging technology products.

For the past 30 years, Sagar has held executive and senior-level operations, engineering, and technology management positions at such analog & mixed-signal companies as Intersil, Maxim, National Semiconductor, Saratoga Semiconductor, and AMD.

Previously an Operating Partner at Khosla Ventures, Sagar continues to serve in board and advisory roles for many system and medical device companies. In addition, he has held board positions in various industry consortiums, including the Global Semiconductor Alliance (GSA) and the Fab Owners Association (FOA). Sagar's mix of experience from technology development to manufacturing gives him a well-rounded view on how to innovate, adapt to new environments, and serve customers that are on the leading edge of technology. Even on the weekends, you'll find him in coffee, lunch, and wine settings, learning about new businesses and how they operate. He thrives on working with customers of all sizes, helping them explore new breakthrough technologies and achieve operational excellence.

Sagar is passionate about expanding IITM alumni outreach in North America, working with the leadership team, esteemed alumni and various local chapters. He intends to collaborate with various support organizations and maximize social, professional and 'giving' initiatives among alumni in North America.

Sagar holds a BTech degree in Chemical Engineering from IIT Madras (1981), and MS in Chemical Engineering from The University of Kansas (1984).